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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re A	Application of:)		
)	Examiner:	Donghee Kang
	Min-hwa Chi)	A T.T *4	2011
Appli	cation No.: 09/865,929)	Art Unit:	2811
Filed:	May 24, 2001))		
For:	TRANSISTOR AND LOGIC)		
	CIRCUIT ON THIN SILICON-ON-)		
	INSULATOR WAFERS BASED)		
	ON GATE INDUCED DRAIN)		
	LEAKAGE CURRENTS)		
)		

Attention: Office of Petitions

Mail Stop Petition

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PETITION TRANSMITTAL

Dear Sir or Madam:

Transmitted herewith is the following:

- 1) Petition to Accept Late Payment of Issue Fee and Publication Fee Under 37 C.F.R. §1.137(a)
- 2) Check No. 3055 in the amount of \$1,740.00
 - a. \$110.00 for Petition to Accept Late Payment of Issue Fee and Publication Fee Under 37 C.F.R.§1.137(a)
 - b. \$300.00 for Publication Fee
 - c. \$1,300.00 for Issue Fee
 - d. \$30.00 for 10 Advance copies
- 3) Issue Fee Transmittal in duplicate

004192.P053D Serial No. 09/865,929 Examiner: Donghee Kang Art Unit: 2811

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- 4) Copy of returned envelope (Exhibit A)
- 5) Copy of previous prepared postcard (Exhibit B)

The Examiner is invited to telephone the undersigned representative if the Examiner believes that an interview might be useful for any reason.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: (lugust 25,2003

Jan Carol Little Reg. No.: 41,181

Customer No. 008791 12400 Wilshire Boulevard, Seventh Floor Los Angeles, California 90025 206-292-8600

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Mail Stop Petition, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:

August 25, 2003

Date of Deposit

Christina Fernandez

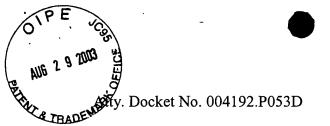
Name of Person Mailing Correspondence

WWWTWW Furnamed August 25, WTS

Signature

Date

004192.P053D Serial No. 09/865,929 Examiner: Donghee Kang Art Unit: 2811



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OFFICE OF PETITIONS

In re Application of:

)
Examiner: Don

Donghee Kang

Min-hwa Chi

Art Unit: 2811

Application No.: 09/865,929

Filed: May 24, 2001

For: TRANSISTOR AND LOGIC

CIRCUIT ON THIN SILICON-ON-INSULATOR WAFERS BASED ON GATE INDUCED DRAIN LEAKAGE CURRENTS

Attention: Office of Petitions

Mail Stop Petition

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PETITION TO ACCEPT LATE PAYMENT OF ISSUE FEE AND PUBLICATION FEE UNDER 37 C.F.R. §1.137(a)

Dear Sir or Madam:

Applicant in the above-identified application has failed to timely pay the Issue Fee and Publication Fee, which were due August 18, 2003. Applicant submits the present Petition, including Remarks, and in papers filed herewith the Issue Fee, Publication Fee and Petition Fee. Applicant respectfully requests that the Petition be granted and late payment of the Issue Fee and Publication Fee be accepted.

Kindly consider the following Remarks, including a Statement Showing Unavoidable

Delay.

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REMARKS

35 U.S.C. §151 authorizes the acceptance of a delayed payment of the Issue Fee if the

Issue Fee is submitted and the delay in payment has been shown to have been unavoidable.

Applicants respectfully submit that the delay in payment of the Issue Fee was unavoidable. (See

also MPEP §703(c) III.A.1.)

Statement Showing Unavoidable Delay

Applicant's representative's employee deposited the proper Issue Fee and Publication Fee

with the United States Postal Service on August 18, 2003, which was the due date for timely

payment. Unfortunately, insufficient postage (37 cents rather than 59 cents) was placed on the

envelope and such error caused the Issue Fee and Publication Fee payment to be delayed.

Applicant's representative has in place a business routine for determining the amount of

postage necessary for mail and most errors are avoided using this procedure. For example,

employees routinely use a postage meter to weigh the mail and automatically affix postage.

Additionally, there are specific examples posted on the wall in the mailroom of the extra postage

required for a particular package. The business routine also requires employees to enter the total

amount of postage per package in a log. The following day, August 19, 2003, Applicant's

representative's employee's supervisor reviewed the log entries in accordance with our business

routine and discovered the error. The envelope containing the insufficient postage and the Issue

Fee and Publication Fee was returned to us on August 20, 2003.

The employee was sufficiently trained in the use of the postage meter and was aware that

extra postage may be required for a particular package. Reliance on the employee represented an

exercise in due care. Such error in the performance of this clerical function was unforeseen from

a trustworthy and reliable employee.

A copy of the original envelope evidencing that the Issue Fee and Publication Fee was

deposited on August 18, 2003 and subsequently returned is enclosed (Exhibit A). A copy of the

postcard is also enclosed (Exhibit B).

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CONCLUSION

The entire delay in paying the Issue Fee and Publication Fee from the due date of the Issue Fee and Publication Fee until the date of the present Petition therefore was unavoidable. Accordingly, Applicants respectfully request that the late payment of the Issue Fee and Publication Fee be accepted and application be revived on the grounds of unavoidable delay.

The Examiner is invited to telephone the undersigned representative if the Examiner believes that an interview might be useful for any reason.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: \$\frac{25}{03}

Jan Carol Little Reg. No.: 41,181

Customer No. 008791 12400 Wilshire Boulevard, Seventh Floor Los Angeles, California 90025 206-292-8600

Examiner: Donghee Kang Art Unit: 2811





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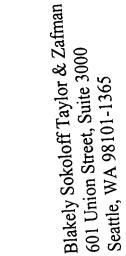
Commissioner for Patent P.O. Box 1450

Mail Stop: Issue Fee

A LIMITED LIABILITY PARTNERSHIP INCLUDING LAW CORPORATIONS

BLAKELY SOKOLOFF TAYLOR & ZAFMAN

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U.S. POSTAGE

EXHIBIT B

09/865,929	May 24, 2001
Serial/Patent No.: 097603, 929 Client: Taiwan Semiconductor Mfg.	Corp.
I PANCTONAN AND TACTO CTDCI	JIT ON THIN SILICON-ON-INSULATOR
Title: BASED ON GATE INDUCED DRA	
BSTZ-File No.: 004192, P053D	Atty/Secty Initials: JCL:cff
Date Mailed: 08/18/2003	Docket Due Date: 08/18/2003
The following has been received in the U.S. Pat	ent & Trademark Office on the date stamped hereon.
Amendment/Response (pgs.)	Express Mail No.: Check No. 3047
Appeal Brief (pgs.) (in triplicate)	Month(s) Extension of Time Am: \$1,630.00
Application - Utility (pgs., with cover and abstract)	Information Disclosure Statement & PTO-1449 (pgx.) [] Check No
Application - Rule 1.53(b) Continuation (pgs.)	Issue Fee Transmittal (IN DUPLICATE)
Application - Rule 1.53(b) Divisional (pgs.)	Notice of Appeal
Application - Rule 1.53(b) CIP (pgs.)	Petition for Extension of Time
Application - Rule 1.53(d) CPA Transmittal (pgs.)	Petition for
- № □ - Application - Design (pgs.)	Postcard
Application - PCT (pgs)	Power of Attorney (pgs.)
Application - Provisional (pgs.)	Preliminary Amendment (pgs.)
Assignment and Cover Sheet	Reply Brief (pgs.)
Certificate of Mailing	Response to Notice of Missing Parts
Declaration & POA (pgs.)	Small Entity Declaration for Indep. Inventor/Small Business
Disclosure Docs & Orig & Copy of Inventor's Signed Letter (pgs.)	Transmittal Letter, in duplicate
Drawings: # of sheets includes figures	Fee Transmittal, in duplicate
Other:	
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